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Applicant: Ohtani, et al.

Serial No.: 08/958,568

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: SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE

Examiner

SAME

Commissioner for Patents Washington, D.C. 20231

REQUEST FOR DRAWING CORRECTION

INITED STATES PATENT AND

Sir:

Authorization is requested to label FIGs. 2A-2F as shown on the attached sheet. They are being labeled to include the terminology "Prior Art" as requested.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

MAR 1 2 2

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

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